

REMARKS

This paper is responsive to the Final Office Action dated February 27, 2004. Claims 3 - 5 are pending in this application and have been rejected. Reexamination is respectfully requested in view of the following remarks.

① At page 3, beginning at line 5 the Examiner has misquoted paragraph (e) of Applicant's claim 1. This paragraph, as well as similar paragraph's in claims 4 and 5 requires as follows:  
"reading twice the same horizontal line data of the field signal in said memory." The Examiner omitted "the same horizontal line data". Still further, the Examiner inserted into the paragraph "the frame" after "such that" and before "the same horizontal line data" at line 7, page 3 of the Office Action.

As stated in claim 3, lines 9 - 11, this invention is characterized by reading and overlapping in the vertical direction the same horizontal line data of field signals. This is not disclosed at all in either the '748 or the '490 reference. Still further, when the references are considered together, there is no suggestion of this claimed non-interlaced scanning signal.

In Applicant's claimed invention, it is the ability to read the same horizontal line data at least twice as illustrated in

Figure 4. Figure 4(A) shows the output from a charge coupled device (13) which is written into a memory M1 (illustrated in Figure 2). Similar field information is written into memory M2 for the even field. The claim language "reading twice the same horizontal line data of the field signal in said field memory" is illustrated in Figure 4(B) where line data  $O_{11}$  is read twice. Applicant respectfully submits that in neither of the cited references Kawai or Watanabe is there a memory the same horizontal line data is read twice as claimed above.

Still further, as claimed, Applicant temporarily writes the signal from the frame memory so that data can be compressed. This concept is totally lacking in either of the references taken singularly or in combination.

In Kawai, the memory shown in Figure 3 receives interlaced NTSC signal at 480 lines. There are two memories used which produce the progressive scanning signal. The switch (19) alternately reads out information from memory (17) and memory (18) to produce the progressive scan as shown in Figure 4. However, neither memory (17) nor memory (18) is read twice. Simply stated, whether memory (17) or memory (18) contains the horizontal line data from the interlaced scanning signal, it is not the same data read twice as claimed.

Lacking this teaching of reading twice the same horizontal line data, the Examiner has asserted that Watanabe '748 suggests providing the read and write clock circuitry. However, Watanabe,

like Kawai, does not provide a read write control circuit for reading twice the same horizontal line data of the field signal.

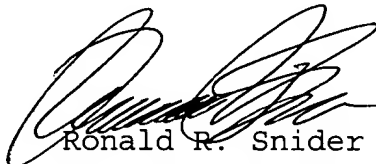
Watanabe, as illustrated in Figure 1, has a single frame memory

(d) where? (131). This frame memory is only read once. Every discussion of frame memory (131) in '748 shows that this memory is read once, and data is subsequently manipulated to provide for decoding and conversion of a digital broadcast signal into a display unit which is analog. The display unit for '748 is described at column 2, line 35 - 43 as being in analog form and digital/analog conversion is used to perform conversion of the video information read from the read clock and supplied to the converted information to the display means. Simply stated, this patent does not suggest or teach the claimed reading twice the same horizontal line data of the field signal. Instead, it is a conversion from one system to another which is an entirely different matter.

2 In summary, this Office Action has failed to address the claimed reading twice of the same horizontal line data of the field signal, and the temporarily writing the signal in a frame memory such that the same horizontal line data of the frame signal can be read twice at double speed of a right speed of the frame signal. Still further, the last part of the claim requires high density data compression in the vertical direction. This feature is also not illustrated in either of the references. Therefore, the elements of the claims have not been met.

In view of the foregoing, it is respectfully submitted that the application is now in condition for allowance, and early action in accordance thereof is requested. In the event there is any reason why the application cannot be allowed in this current condition, it is respectfully requested that the Examiner contact the undersigned at the number listed below to resolve any problems by Interview or Examiner's Amendment.

Respectfully submitted,



Ronald R. Snider  
Reg. No. 24,962

Date: April 22, 2004

Snider & Associates  
Ronald R. Snider  
P.O. Box 27613  
Washington, D.C. 20038-7613  
(202) 347-2600

RRS/bam